

FACULTY PROFILE

Name of the faculty: VANKALOPALLI VITTAL REDDY

Department: Electronics and Communication Engineering

College: Gudlalleru Engineering College

Mobile: 9440735177

E-Mail: vankalopalli@gmail.com

Address for Communication

S/O V. Ramachandra Reddy

D.No:3/196-3

Main Road

Gudlalleru,

KRISHNA- 521356

Academic Qualifications

S. No	Name of the Degree	University	Percentage of Marks/Grade	Specialization	Year of Pass
1	M.Tech	J.N.T.U, Kakinada	73.42	Instrumentation and Control Systems	2004
2	B.Tech(E.C.E)	Nagarjuna University	68.6	E.C.E	2000
3	Intermediate	Board of Intermediate Education, A.P	75.6	M.P.C	1994
4	S.S.C	Board of Secondary Education, A.P	73.5	---	1992

Professional Experience

S. No	Designation	Institution Name	Working Period	
			From	To
1	Associate Professor	Gudlavalleru Engineering College, Gudlavalleru	25-08-2005	Till date
2	Assistant Professor	Gudlavalleru Engineering College, Gudlavalleru	09-09-2000	24-08-2005

Professional Body Membership

S. No	Name of the Professional body	Membership Number
1	The Institution of Electronics and Telecommunication Engineers (I.E.T.E)	M188581
2	The Indian Society for Technical Education(I.S.T.E)	LM44102
3	Society for Learning Technologies	LM475

Papers Published in Journals No. : 18

1. V. Vittal Reddy, Implementation of Digital Filter Using Verilog HDL. International Journal for Research in Applied Science & Engineering Technology, Volume 12, Issue IV, April2024.
2. V. Vittal Reddy, M. Neelaveni, M. Sandeep, Sk. Khaleel Ahmad, P. Vamsi Krishna Comparison of FIR And IIR Filters using ECG Signal with different sampling frequencies.International Research Journal of Modernization in Engineering Technology and Science. Volume: 05, Issue: 04,ISSN: 2582-5208, April-2023.
3. V. Vittal Reddy, Implementation of High Speed and Area Efficient 1024-Point Radix-2 Fast Fourier Transform Processor. Journal of Interdisciplinary Cycle Research Volume: 15, Issue: 04, April- 2023.

4. V. Vittal Reddy, M. Phani Kumar K.Naveen, L.B.N.V. Kailash Kumar, Hiding Image-Into Audio Steganography By Using CNN. International Journal of All Research Education and Scientific Methods Volume: 10, Issue: 04, April- 2022.
5. V Vittal Reddy, N Naga Lakshmi, VHDL Implementation of Energy Efficient Multiplier using Bit Significance Driven Logic Compression. International Journal of Engineering Research & Technology. Volume:8, Issue: 07, July-2019.
6. A.Roja Varalakshmi, V.Vittal Reddy, "Design of an efficient SAR-GMTI for target detection and radial velocity estimation, International Journal of Advanced Research and latest trends Vol.10 Issue.02, ISSN: 3011-3030 May-2018.
7. V.Lalitha, V.Vittal Reddy, "A novel approach for minimizing energy utilization and maximizing network lifetime for mobile wireless sensor networks", International Journal of Engineering Development and Research, Vol.5, Issue.3, ISSN: 2321-9939-2017.
8. Sk.Sirajuddin, V.Vittal Reddy, "Energy Efficiency Analysis for MIMO Communication Systems", International Journal of Advanced Research in Computer and Communication Engineering, Vol.5, Issue.9, ISSN: 2278-1021, Sep-2016.
9. T.Sridhar, V.Vittal Reddy, "A Novel Method for Improving the Throughput in High Data rate WSNs" International Advanced Research Journal in Science, Engineering and Technology, Vol. 3, Issue 8, ISSN (Print) 2394-1588, August 2016.
10. B.Ramakrishna Naik, V.Vittal Reddy, "Geographic-Based routing protocol for Heterogenous Mobile Ad-Hoc Networks", International Journal of Scientific & Engineering Research, Vol.5, Issue.12, ISSN: 2229-5518, Dec-2014.
11. M.Naveen Kumar, V.Vittal Reddy, "Dynamic Data Polarization Technique for Improving Quality of Service in Wireless System Network", **International** Journal of Engineering Research & Technology, Vol.3, Issue.11, ISSN: 2278-0181, Nov-2014.
12. T.K.L.Prasanna, V.Vittal Reddy, M.V.Somashekhar Utility Interactive Electricity Generation Schemes with renewable Resources" IOSR Journal of Electronics and Communication Engineering, vol.07, issue.03, e-ISSN: 2278-8735, Sept-Oct, 2013, pp-01-06, Impact factor: 1.682
13. Sk.Fahiya, V.Vittal Reddy, N.Nagewara Rao "Analysis and Testing of VME(Versa Modular European) Bus cards" International Journal of Computer Applications, vol.78.No.15, ISSN: 0975-8887, Sept, 2013, pp.5-12, Impact factor: 0.85
14. Pattan Munni, V.Vittal Reddy, N.Nageswara Rao "Simulation of Signals with Field Signal Simulator" IOSR Journal of Electronics and Communication Engineering, vol.07,issue.03,p-ISSN:2278-2834,Sept-Oct 2013, pp-07-12,Impact factor:1.682.

15. P. Ashok Babu, V.Vittal Reddy, "Simulation and synthesis of an AM software Radio Receiver", International Journal of VLSI and Embedded Systems-IJVES, ISSN: 2249-6556, Pp.181-184, Vol.3, Issue.5, November-December 2012, Impact Factor: 1.97.
16. R. Sudha Rani, Vittal Reddy V, "Analysis and Design of Spreading Code Using ZCZ", International Journal of Engineering Science and Technology (IJEST), ISSN: 0975-5462, Vol.3, No.7, July 2011, Impact factor: 1.844.
17. V. Vittal Reddy, K.Vindya Smitha , "Zener Diode characteristics and load line using MATLAB", KamalJeeth Instrumentation & Services unit National Journal, LE. Vol.-10, No.-3, pp.199-204, Sept., 2010.
18. V.Vittal Reddy, K.Vindhya Smitha, "Maximum power transfer theorem using MATLAB", KamalJeeth Instrumentation & Services unit National Journal, LE. Vol.-10, No.-2, pp.120-124, June 2010.

Papers Published in Conferences No. :02

1. Damodhara Rao M, V.Vittal Reddy, "Implementation of improved redundant Binary Booth Encoding for Fast Multiplier", IEEE International Conference on Electronics communication & Computing Technologies, (ICECCT'11), PP: 1-7, September 12-13, 2012.
2. M.Kamaraju, V.Vittal Reddy, K.Vindhya Smitha, " Teaching Methodology – Newton and Galileo", ISTE Fourth Annual Convention and Awards Function, pp. 61-67, 21-22 Nov 2009.

Workshops Organized No. : 02

1. A two week ISTE workshop on "Signals and Systems" conducted by IIT Kharagpur from 2nd to 12th January 2014 at Gudlavalleru Engineering College.
2. A Five day workshop on "DSP Processors" from 22-26 sep 05 conducted by Cranes Varsity (A Division of Cranes Software International Limited) at Gudlavalleru Engineering College, Gudlavalleru.

List of online workshops/conferences/seminars attended: 07

1. One Week Online Faculty Development Program on “Applications of Computer Vision using Deep Learning” conducted by the department of Electronics & Communication Engineering, Seshadri Rao Gudlavalleru Engineering College during 20-25th February 2023.
2. A National Level Webinar on “Intellectual Property Rights & Patents - A View” organized by IQAC, Gudlavalleru Engineering College, Gudlavalleru held during 10 - 12 August 2020.
3. A National Level Online Faculty Development Program on “Cyber Security” organized by Departments of Computer Science and Engineering & Information Technology in association with CyberPsy, Gudlavalleru Engineering College, Gudlavalleru from 22nd-26th July, 2020.
4. Online Faculty Development Program on “MATLAB and Simulink in Engineering Education “organized by APSSD from 10-06-2020 to 11-06-2020.
5. One Week International online Knowledge Development Program on “Challenges and Advancements in the Design of IoT ,Embedded and VLSI Systems: A Researchers View “organized by Department of Electronics & Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru from 8th-13th June, 2020.
6. One Week Online Faculty Development Program on Speech Processing Applications & Challenges Organized by Department of ECE, SANTHIRAM ENGINEERING COLLEGE, Nandyal in collaboration with International Institute of Information Technology, Hyderabad from 20-05-2020 to 25-05-2020.
7. One Week Online Faculty Development Program on “Advanced Antenna Design Using HFSS” Organized by Department of ECE ,Santhiram Engineering College ,Nandyal in collaboration with Vardhaman College of Engineering ,Hyderabad from 12-05-2020 to 17-05-2020

Workshops /Conferences /Seminars Attended No. : 21

1. One Week Refresher Course on “Transmission Lines and Waveguides ”organized by Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College during 29th January–2nd February 2024.
2. One Week Refresher Course on “Microcontrollers and Embedded Systems ”organized by Department of Electronics and Communication Engineering Seshadri Rao Gudlavalleru Engineering College during 9th January–13th January 2024.

3. A one week national level faculty development programme on Signal and Image Processing using LABVIEW held during 13-18 November 2017 at Gudlavalleru Engineering College, arranged by Signal Processing Research group of department of Electronics and Communication Engineering in association with NI Systems Pvt. Ltd.
4. A two day faculty training programme on Advances in VLSI Design and Embedded Systems organized by department of electronics and communication engineering Gudlavalleru Engineering College, Gudlavalleru during 6th-7th November 2017.
5. A One week Faculty Development Program on Hands on experience in signal processing applications held during 7th-12th November 2016 at Gudlavalleru Engineering College.
6. A Two day staff training program on " Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications" using NI Multisim organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 4-5 November 2016.
7. A One week Faculty Development Program on "Lines, Fields & Waves: The Transmission of Energy Jointly organized by Electronics & ICT Academy NIT, Warangal and Department of ECE, Sri Vasavi Engineering College, Tadepalligudem during 14-24 December, 2015.
8. A One day workshop on Research in Energy Management Wireless Networks & Smart Transportation Systems organized by I.E(INDIA), Vijayawada Local Centre on 29th March 2015.
9. A One day workshop on "Ultra Low Power Communication using CC430 Microcontroller: Hands on Experience" on 7th February, 2015 organized by Signal Processing & Communications Research Group, Department of E.C.E , Gudlavalleru Engineering College.
10. A one day workshop on "Speech Processing: Current Challenges and Hands on Experience" organized by Signal Processing and Communications Research Group, Department of E.C.E, Gudlavalleru Engineering College on 23rd August 2014.
11. A Three day workshop on "Theory & Applications of Intelligent Signal Processing" by Dr. Ganapathi Panda, Professor, IIT Bhubaneswar and Mr. Shakar Saragadam, Scholar, School of Electrical Sciences, IIT Bhubaneswar organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 28th February to 2nd March 2014.

12. A One Week Coordinators workshop on “Signals & Systems”, conducted by Indian Institute of Technology Kharagpur under the National Mission on Education through ICT (MHRD) during 30th September to 4th October 2013.
13. A Two day workshop on “ISSUES IN INTEGRATED CIRCUITS (ICs) DESIGN”, by Dr. K.S.R. Krishna Prasad, Professor, ECE Dept., NIT, Warangal organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru during 13th to 14th December 2013.
14. A Two week workshop on “Analog Electronics”, organized by Department of ECE, Gudlavalleru Engineering College, Gudlavalleru in association with IIT Kharagpur under NMEICT (MHRD) during 4th to 14th June 2013.
15. Five Day Training Program on “Mentor Graphics and Xilinx Tools “organized by department of Electronics and Communication Engineering, Gudlavalleru Engineering College in association with IETE Vijayawada Centre & Corel Technologies, Bangalore from 13th to 17th of September 2010.
16. A workshop on Verilog Programming & Implementation during 27-28 October 2007 organized by the Department of Electronics and Communication Engineering, Gudlavalleru Engineering College.
17. A workshop on “VLSI Design” during 9th & 10th February 2007 organized by the Department of Electronics and Communication Engineering and IEEE Student Branch of Gudlavalleru Engineering College.
18. A Short term Course on “Instructional Design and Delivery” conducted by NITTTR from 3rd to 8th July 2006.
19. A National Workshop on Advances in Signal and Image Processing during 27th & 28th February, 2006 organized by the Department of E.C.E, JNTU, College of Engineering, Kakinada, A.P.
20. A Refresher Course on “Digital Signal Processing” from 17th November to 7th December 2004 organized by the JNTU, Hyderabad, A.P.
21. A Short term Course on “Instructional Design and Delivery “conducted by NITTTR from 21st to 26th April 2003.

Subjects Handled

Under Graduation

- Probability Random Variables and Random Processes.
- Electromagnetic Waves and Transmission Lines.
- Antennas and Wave Propagation.
- Digital Signal Processing.
- Communication Theory.
- Radar Engineering.
- Transmission Lines and Wave Guides.
- Analog and Digital Communications.
- Linear Integrated Circuits Applications.
- Satellite Communications.
- Signals and Modulation Theory.
- Coding Theory.

Post Graduation

- Detection and Estimations of Signals.
- Coding Theory and Techniques.

Other Responsibilities

College Level

1. Member- Recruitment
2. Invitee- CMC

Department Level

In charge-EDC Laboratory.

Projects Guided

Under Graduation: 34

Post-Graduation: 10

Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 1-2-2025

Place: Gudlavalleru

Signature

(V.Vittal Reddy)